

Attorney Docket No. RA019C3DR

Express Mail No. EK 100 209 556 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Richard M. Barth et al.

Application No.: Not yet assigned

Filed: Herewith

For: Re-Issue of U.S. Patent No. 5,765,020
issued June 9, 1998 and entitled:

METHOD OF TRANSFERRING DATA BY
TRANSMITTING LOWER ORDER AND UPPER
ORDER MEMORY ADDRESS BITS IN SEPARATE
WORDS WITH RESPECTIVE OP CODES AND
START INFORMATION

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Sir:

Prior to examination, please amend the above-identified reissue application as follows:

IN THE CLAIMS:

Please add new claims 12-41 as follows:

12. A method of operation in a memory device, the memory device having an array of memory cells, the method comprising:
receiving a first column address, the first column address representing a column locality of
a first storage location within a first row in the array;

009240-586550

#5/a
P. Barth
7-9-02